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THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

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ASSIGNOR:

APRIL 01, 1999

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DOC DATE: 04/06/1998

ASSIGNEE:

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SERIAL NUMBER: 09057158

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FILING DATE: 04/07/1998

ISSUE DATE:

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DATES ENTERED: WAY

APR 1 2 1999

GRAHAM & JAMES (PA) IP DEPT.





COMBINED DECLARATION FOR PATENT APPLICATION & POWER OF ATTORNEY

DOCKET NO.: 49259.00002

As a below named inventor, I hereby declare that:

The information given herein is true;

COPY

My residence, post office address and citizenship are as stated below next to my name;
I BELIEVE I AM THE ORIGINAL, FIRST AND SOLE INVENTOR (if only one name is listed below) OR AN ORIGINAL,
FIRST AND JOINT INVENTOR (if plural names are listed below) OF THE SUBJECT MATTER WHICH IS CLAIMED AND
FOR WHICH A PATENT IS SOUGHT ON THE INVENTION ENTITLED:

High Speed Source Synchronous Signaling For Interfacing VLSI CMOS Circuits To Transmission Lines

the specification of which (check only one item below):

X D	is attached hereto; was filed on	United States	
	Application Serial Noand was amended on		ole).
0	was filed on	as PCT International	
	Application Serial No		_ (if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations Section 1.56(a).

I hereby claim the benefit under Title 35, United States, §119(e) of any United States provisional application(s) listed below.

High Speed Source Synchronous Signaling for Interfacing VLSI CMOS Circuits to Transmission Lines (Title)

3/16/98 (Filing Date) Ejaz Ul Haq Inventor

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

FOREIGN APPLICATION(S), IF ANY, FILED WITHIN 12 (6 if a Design) MONTHS PRIOR TO THE FILING DATE OF THIS APPLICATION THE PRIORITY OF WHICH WHERE PERMITTED IS HEREBY CLAIMED UNDER 35 U.S.C. SEC. 119.

COUNTRY	APPLICATION OF NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)	PRIORITY · CLAIMED	
				yes	
	·			. , yes	
				yes	

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

	COMBINED DECLARATION FOR PAT (APPLICATION & POV		· · ·		TORNEY'S DOCKET NO: 259.00002		
	U.S. APPLICATION	ON NO.	S. FILING DATE	PATENTED		PENDING	ABANDONED
}							
1	PCT APPLICATION						
ł	PCT APPLICATION	ON NO.	PCT FILING DATE	U.S. SERIAL NUMBERS			
	POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attomey(s) and/or Agent(s) to prosecute this application and transact ail business in the Patent and Trademark Office connected therewith. David L. Fehrman, Reg. No. 28,600;David L. Henty, Reg. No. 31,323;William J. Robinson, Reg. No. 29,430;Stuart L. Merkadeau, Reg. No. 33,262;David B. Abel Reg. No. 32,394;Hisako Muramatsu, Reg. No. 34,955;Brian M. Berliner, Reg. No. 34,549;David J. Meyer, Reg. No. 33,425;Vincent J. Belusko, Reg. No. 30,820; Minda Schechter, Reg. No. 38,296;William G. Anderson, Reg. No. 27,851;Victor De Gyarfas, Reg. No. 40,583;Dennis R. Gallagher, Reg. No. 42,563;Jonathan L. Pettit, Reg. No. 40,770;Wayne Smith, Reg. No. 42,160;Lawrence W. Granatelli, Reg. No. 32,228;Laura A. Majerus, Reg. No. 33,417;Joseph K. Hollinger Reg. No. 40,649;Jonathan T. Kaplan Reg. No. 38,935;Marc Sockol Reg. No. 40,823; lan Cartier, Reg. No. 38,406;LInda Beach Reg. No. 36,446.						
	Send correspon	dence to	GRAHAM & JAMES LLP 600 Hansen Way		Direct Phone	e Calls To: Franatelli 650-856-650	20
\dashv		т	Palo Alto, CA 94304-1043	<u>, , , , , , , , , , , , , , , , , , , </u>		1	
1	FULL NAME OF INVENTOR	LAST NAME		FIRST NAME	•	MIDDLE NAME	Ĭ
	RESIDENCE &	CITY		STATE OR FOREIGN COUNTR		COUNTRY OF CITIZENSHIP	
	CITIZENSHIP	SUNNYVALE		CALIFORNIA	, \$	U.S.	· I
	POST OFFICE	STREET		CITY		STATE OR	94087
	ADDRESS	864 TALISMAN	1 DRIVE	SUNNYVALE		COUNTRY	
_	5141 214245	. ACT NAME		SIROT MANE		CALIFORNIA	<u> </u>
2	FULL NAME OF INVENTOR	LAST NAME		FIRST NAME		MIDDLE NAME	
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	POST OFFICE ADDRESS	STREET		CITY		STATE OR COUNTRY	ZIP CODE
3	FULL NAME LAST NAME OF INVENTOR		FIRST NAME		MIDDLE NAME		
	RESIDENCE & CITIZENSHIP	CITY		STATE OR FOREIGN COUNTR	₹Y	COUNTRY OF CITIZENSHIP	
	POST OFFICE ADDRESS	STREET		СІТУ		STATE OR COUNTRY	ZIP CODE
4	FULL NAME OF INVENTOR	LAST NAME		FIRST NAME		MIDDLE NAME	
	RESIDENCE & CITIZENSHIP	CITY		STATE OR FOREIGN COUNTR	RY	COUNTRY OF CIT	IZENSHIP
	POST OFFICE ADDRESS	STREET		CITY	•	STATE OR COUNTRY	ZIP CODE
	I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.						
SIG	SIGNATURE OF INVENTOR 1 SIGNATURE OF INVENTOR 2 SIGNATURE OF INVENTOR 3 SIGNATURE OF INVENTOR 4					IVENTOR 4	
DATE U-6-1998 DATE DATE DATE			DATE				

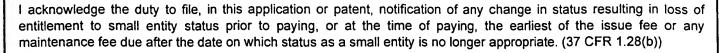
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VERIFIED STATEMEN LOCLARATION) CLAIMING SMAL Docket No. 49259.00002 STATUS (37 CFR 1.9(f) AND 1.27 (b)) - INDEPENDENT INVENTOR Serial No. Filing Date Patent No. Issue Date Unassigned Unassigned Unassigned Unassigned Applicant/ Ejaz Ul Haq Patentee: High Speed Source Synchronous Signaling for Interfacing VLSI CMOS Circuits to Transmission Lines Invention: As a below named inventor, I hereby declare that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees under section 41(a) and (b) of Title 35, United States Code, to the Patent and Trademark Office with regard to the invention entitled above and described in: the specification to be filed herewith. the application identified above. ☐ the patent identified above. I have not assigned, granted, conveyed or licensed and am under no obligation under contract or law to assign, grant, convey or license, any rights in the invention to any person who could not be classified as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e). Each person, concern or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention is listed below: No such person, concern or organization exists. ☐ Each such person, concern or organization is listed below. *NOTE: Separate verified statements are required from each named person, concern or organization having rights to the invention averring to their status as small entities (37 CFR 1.27) **FULL NAME ADDRESS** Nonprofit Organization Individual Small Business Concern **FULL NAME ADDRESS** Nonprofit Organization Small Business Concern Individual **FULL NAME ADDRESS** Nonprofit Organization Individual Small Business Concern **FULL NAME ADDRESS**

Small Business Concern

Individual

Nonprofit Organization



I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

NAME OF INVENTOR <u>Ejaz UI Haq</u>		
SIGNATURE OF INVENTOR	DATE:	4-6-1998
NAME OF INVENTOR		
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Attorney Docket No. 49259.00002

	(1) Ejaz Ul Haq	(5)
(1-8) Insert Name(s) of Inventor(s)	(2)	(6)
	(3)	(7)
	(4)	
For good and valuable consideration hereby do(es) assign, transfer and set	receipt of which is hereby over to:	acknowledged, the undersigned agree(s) to assign, and
(9) Insert name of Assignee	(9) Jazio, Inc.	
(10) Insert state of incorporation of Assignee	(10) <u>California</u>	
(11) Insert address of Assignee	(11) of <u>864 Talisman D</u> (hereinafter designated interest in the invention	rive, Sunnyvale, CA 94087 as the Assignee) the entire worldwide right, title and known as
(12) Insert Identification of Invention, such as Title, Case Number or Foreign Application Number	(Case No. 49259 0000	lling for Interfacing VLSI CMOS Circuits D2) (Serial No. 09/057,158) for which the undersigned has lication for patent in United States of America
(13) Insert Date of Signing of Application	(13) on <u>4/6/98</u>	
declared concerning this application possible in obtaining evidence and go	or continuation or division into the forward with such into	ecessary in connection with any interference which may be a thereof and to cooperate with the Assignee in every way reference. rs and documents and perform any act which may be national Convention for Protection of Industrial Property or
_	s) to perform all affirmation	re acts which may be necessary to obtain a grant of a valid
5) The undersigned hereb authorities of foreign countries to is divisions or continuing or reissue appraise significant title and interest and he	y authorize(s) and requestive any and all Letters discations thereof to the same	st(s) the Commissioner of Patents and the duly constituted Patents resulting from said application or any division or id Assignee, its successors and assigns, as Assignee of the as (they have) full right to convey the entire interest herein t execute, any agreement in conflict herewith.
6) The undersigned hereb	y grant(s)	
Reg. No. 33,262;David B. Abel Re Meyer, Reg. No. 33,425;Vincent 40,583;Dennis R. Gallagher, Reg. Granatelli, Reg. No. 32,228;Laura 40,823;Linda Beach Reg. No. 36,4	g. No. 32,394;Hisako Muramat I. Belusko, Reg. No. 30,820; I No. 42,563;Wayne Smith, Reg. A. Majerus, Reg. No. 33,417; 46; Harmohinder S. Bedi Reg.	,323;William J. Robinson, Reg. No. 29,430;Stuart L. Merkadeau, su, Reg. No. 34,955;Brian M. Berliner, Reg. No. 34,549;David J. Alinda Schechter, Reg. No. 38,296;Victor De Gyarfas, Reg. No. No. 42,160; Stefan J. Kirchanski, Reg. No. 36,568 Lawrence W. Joseph K. Hollinger Reg. No. 40,649; Marc A. Sockol Reg. No. No. 39,904; Benjamin Rubin Reg. No. P44,310;Leah Sherry Reg.
the power to insert on this assignment with the rules of the United States P.	atant and Trademark Cilli	on which may be necessary or desirable in order to comply see for recordation of this document.
Date DEC. 10.1998	Na	ne of inventor(s)
Date	Na:	me of Inventor(s)
		me of Inventor(s)
Date	Na	
Date		me of Inventor(s)

57483.01.00 J8/1150/49259.00002

9. Statement and signature.

To the best of my knowledge and belief, the foregoing Information is true and coffect and any attached copy is a true copy

of the original document

Marc A. Sockol

Name of Person Signing

Signature

12-11-98

Date

Total number of pages including cover sheet, attachments, and